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plurality of display pixel structures, each of the plurality of display pixel structures comprising a display transistor and a display storage capacitor.

17. A method for controlling an integrated touch panel LCD device, the integrated touch panel LCD device comprising a liquid crystal panel, the liquid crystal panel comprising a plurality of integrated pixel structures, a plurality of data lines, and a plurality of gate lines,

wherein each of the integrated pixel structures includes: a transistor matrix substrate, a color filter substrate disposed above the transistor matrix substrate, the color filter substrate being substantially parallel with the transistor matrix substrate, and a liquid crystal layer interposed between the transistor matrix substrate and the color filter substrate, and

wherein the transistor matrix substrate comprises: a first transistor, a first storage capacitor connected to the first transistor, a second transistor, a second storage capacitor connected to the second transistor, and a conductive protrusion disposed corresponding to the second storage capacitor and configured to electrically connect the second storage capacitor and the color filter substrate when the color filter substrate is pressed and configured not to electrically connect the second storage capacitor and the color filter substrate when the color filter substrate is not pressed, wherein charges in the second storage capacitor are discharged through the conductive protrusion and the color filter substrate when the second storage capacitor electrically connects to the color filter substrate which is pressed, the method comprising the steps of: transmitting display data to the first storage capacitors for updating a display image of the liquid crystal panel; transmitting touch reference data to the second storage capacitors; reading data stored in the second storage capacitors; and calculating the stored data to obtain touching information corresponding to a touch position on the liquid crystal panel.

18. The method according to claim 17, wherein the first and the second transistors of each of the plurality of integrated pixel structures respectively connect to two gate lines of the plurality of gate lines, and both connect to one data line of the plurality of data lines, the method further comprising:

turning on the first transistors of the plurality of integrated pixel structures sequentially during a first time period

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for transmitting the display data to the first storage capacitors through the first transistors; turning on the second transistors of the plurality of integrated pixel structures sequentially during a second time period for transmitting the touch reference data to the second storage capacitors through the second transistors; turning on the first transistors of the plurality of integrated pixel structures sequentially during a third time period for transmitting the display data to the first storage capacitors through the first transistors; and turning on the second transistors of the plurality of integrated pixel structures sequentially during a fourth time period for reading the data stored in the second storage capacitors through the second transistors.

19. The method according to claim 17, wherein the first and the second transistors of each of the plurality of integrated pixel structures respectively connect to two data lines of the plurality of data lines, and both connect to one gate line of the plurality of gate lines, the method further comprising:

turning on the first and the second transistors of the plurality of integrated pixel structures sequentially during a first time period for transmitting the display data to the first storage capacitors through the first transistors, and transmitting the touch reference data to the second storage capacitors through the second transistors; and turning on the first and the second transistors of the plurality of integrated pixel structures sequentially during a second time period, for transmitting the display data to the first storage capacitors through the first transistors and reading the data stored in the second storage capacitors through the second transistors.

20. The method according to claim 17, further comprising: comparing the stored data and the touch reference data to obtain the touching information.

21. The method according to claim 17, further comprising: setting a threshold voltage; and comparing the stored data and the threshold voltage to obtain the touching information.

22. The method according to claim 17, wherein the display data and the touch reference data are the same.

23. The method according to claim 17, wherein the touching information comprise coordinate of the touch position, touching area, or pressure on the touch position.

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